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M68000-compatible, high-performance, 32-bit microprocessors,
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MC68040 Datasheet(PDF) - Motorola, Inc

mode capabilities of the MC68020, MC68030, and MC68040 have
been maintained, allowing a clear migration path for M68000
systems. The main features of the MC68EC030 are as follows: •

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Object-Code Compatible with the MC68020, MC68030, and Earlier M68000 Microprocessors • Burst-Mode Bus Interface for Efficient DRAM Access

MOTOROLA SEMICONDUCTOR TECHNICAL DATA
MC68EC030

MOTOROLA INC., 1992 MOTOROLA M68000 FAMILY
Programmer's Reference Manual (Includes CPU32 Instructions) F
r e e s c a l e S e m i c o n d u c t o r , I Freescale Semiconductor,
Inc.

MOTOROLA M68000 FAMILY - NXP Semiconductors

Motorola 68020 (MC68020) is a 32-bit microprocessor compatible with earlier members of 680x0 family - 68000, 68008 and 68010. Address bus width on the 68020 is increased to 32 bits, which allows the processor to address up to 4 GB of memory.

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Motorola 68020 (MC68020) microprocessor family

The MC68030 contains all features of the MC68020, plus some additional ones. The basic differences between the MC68020 and MC68030 are as follows: 11.7.3 Motorola MC68040 I MC68060. This section presents an overview of the Motorola MC68040 and MC 68060 32-bit microprocessors.

Motorola MC68030 , Motorola MC68040 I MC68060 , PowerPC ...

Introduction This is my attempt to write something about my experiments with the MC68020 processor. It is a desire to finally try to make my own MC68020 board, which ultimately should also have the MC68851 PMMU and the MC68881/68882 FPU to make it complete. I agree that using a MC68030, MC68040 or MC68060 make my...

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MC68040 - Wikipedia

mc68030 - 1987 m68000 32
68020 68040

MC68030 - Wikipedia

Motorola MC68020 . The MC68020 was launched in April 1984 as the '32 bit performance standard' and in those days its performance was simply staggering — 8 million instructions per second peak with 2-3 million sustained when running at 16 MHz clock speed.

Motorola MC68020 - BrainKart

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MOTOROLA M68000 FAMILY - UVa

Memory management has now been brought on-chip with the

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MC68030 using a subset of the MC68851 PMMU with a smaller 22 entry on-chip address translation cache. The 256 byte instruction cache of the MC68020 is still present and has been augmented with a 256 byte data cache.

Motorola MC68030 - BrainKart

capabilities of the MC68030 microprocessor with independent 4K-byte data and instruction caches and an on-chip FPU. The MC68040 maintains the 32-bit registers available with the entire M68000 Family as well as the 32-bit address and data paths, rich instruction set, and versatile addressing modes. Instruction execution proceeds in ...

The M68000 Family - OK1MJO

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MOTOROLA M68000 FAMILY - ticalc.org

Introduction 1.2 MC68030 EXTENSIONS TO THE M68000 FAMILY
In addition to the on-chip instruction cache present in the
MC68020, the MC68030 has an internal data cache. Data that is
accessed during read cycles may be stored in the on-chip cache,
where it is available for subsequent accesses.

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manual / Published: (1986) System design with the MC68020,
MC68030, and MC68040 32-bit microprocessors / by: Noor,
Asghar. Published: (1994) How to program and interface the
6800 / by: Staugaard, Andrew C

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